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Attorney Docket No. 122.1444D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Noriyuki ITO et al.

Application No.: 10/625,631

Group Art Unit: 2825

Confirmation No. 8072

Filed: July 24, 2003

Examiner: LIN, SUN J

For: PLACEMENT/NET WIRING PROCESSING SYSTEM

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure provisions of 37 CFR § 1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the subject application.

Respectfully submitted,

STAAS & HALSEY LLP

Dated: 20 JUL 07
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Registration No. 37,889



Sheet 1 of 1

INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>	PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY DOCKET NO. 122.1444D	APPLICATION NO. 10/625,631
	FIRST NAMED INVENTOR Noriyuki ITO et al.			
	FILING DATE July 24, 2003		GROUP/ART UNIT 2825	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA						
	AB						
	AC						
	AD						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	TRANSLATION YES NO		ABSTRACT
	AE	10-269272	09/10/1998	Japan			YES
	AF	2000-259677	09/22/2000	Japan			Yes
	AG						
	AH						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

				TRANSLATION YES NO	
	AI	Design Wave Planning Office, "Design and Simulation of Digital Electronic circuit with PC", CQ Publishing Company, third edition, pages 47, 48 and 179			X

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

**ATTACHMENT 1(e)**

EXPLANATIONS OF RELEVANCY OF REFERENCES	ATTORNEY DOCKET NO.	APPLICATION NO.
	122.1444D	10/625,631
	FIRST NAMED INVENTOR	
	Noriyuki ITO et al.	
	FILING DATE	GROUP ART UNIT
	July 24, 2003	2825

Design Wave Planning Office, "Design and Simulation of Digital Electronic circuit with PC", CQ Publishing Company, third edition, pages 47, 48 and 179 (Reference AI). A method for checking a circuit designed by PC is disclosed at pages 47 and 48. At page 179, it is described that when a wire graphic displayed on a screen is clicked, connecting information of the wire is displayed on the screen.

**ATTACHMENT 1(g)**

LIST OF ADDITIONAL SUBMITTED DOCUMENTS	ATTORNEY DOCKET NO.	APPLICATION NO.
	122.1444D	10/625,631
	FIRST NAMED INVENTOR	
	Noriyuki ITO et al.	
	FILING DATE	GROUP ART UNIT
	July 24, 2003	2825

The following document(s) is/are listed in accordance with the duty of disclosure provisions of 37 CFR § 1.56, so that the Examiner may consider same should he deem any thereof to be material to examination of the subject application.

It is requested that the Examiner acknowledge his consideration of document(s) below-listed by initialing same in the space provided adjacent each such application and that the Examiner sign and date this form at the bottom thereof to confirm such consideration having been given.

This submission in no way represents an admission that any of the information listed herein constitutes prior art with respect to the subject application; and unless and until such prior art status is established, this submission is not a request that the information presented herein be printed on the face of any patent issuing from the subject application in which this information is being filed.

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA						
	AB						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO	
	AC							
	AD							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			TRANSLATION YES NO	
	AE	Japanese Office Action mailed May 29, 2007 in corresponding Japanese Patent Application No. 2000-304083	Partial	

EXAMINER	DATE CONSIDERED
*EXAMINER: Initial if document considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	